

21<sup>st</sup> International  
Conference On VLSI Design



2008



7<sup>th</sup> International Conference  
On Embedded Systems

4th - 8th JANUARY, 2008 at HICC, Hyderabad - India

Call for Participation | Registration | Conference Program | Sponsorship Opportunities | Location | Team Members | Contact Us

SUPPORTED BY



Sponsors



General Instructions

Registration

This joint-conference is a forum for researchers and designers to present and discuss various aspects of VLSI design, electronic design automation (EDA), enabling technologies, and embedded systems. It covers the entire spectrum of activities in the two vital areas of VLSI and embedded systems, which underpin the semiconductor industry.

The five-day program will consist of regular paper sessions, special sessions, embedded tutorials, panel discussions, design contest, industrial exhibits and two days of tutorials. The 2008 conference has a particular emphasis on the challenges and opportunities in Nanoelectronics, as we scale the CMOS technology in the Nanometer dimension.

The new paradigms in design, EDA and system implementation will be presented and discussed in the 2008 edition of this conference.

Registrations are started  
Register before Nov 30 for special discounts.

Exhibit Booth 44 remaining out of 72 Exhibit booths

For hotel accommodation please contact accommodation@vlsiconference.com

Gold 	Gold 	Gold 
Gold 	Gold Available	
SILVER 	SILVER 	SILVER 
SILVER 	SILVER 	SILVER 
SILVER Available		

Bronze

		Available	Available	Available
Available	Available	Available	Available	Available

Venue



VLSI NEWS

» VLSI Opening Video 2008

1. Tutorials will be held on Jan 4 and 5. Technical Program will follow on Jan 6, 7 and 8.
2. Tutorial Program has been schedule in conference program .

Fellowship Accomodation Information

- » Hotel Details
- » Room Allocations

- » Conference Archives
- » Discussion Forum
- » VLSI Community

Join the  
VLSI  
Community



4th - 8th JANUARY, 2008 at HICC, Hyderabad - India

[Call for Participation](#) | [Registration](#) | [Conference Program](#) | [Sponsorship Opportunities](#) | [Location](#) | [Team Members](#) | [Contact Us](#)

[Registration](#) ▾

[Home](#)

▾ [General Instructions](#)

▾ [Registration](#)

[Registration](#) ▾

[Registration](#)

Dear Participant,

The 21st International Conference on VLSI Design and the 7th International Conference on Embedded Systems are being held jointly from 4th to 8th January 2008 in Hyderabad. The conference is celebrating the 21st edition through commemorative issue(s) and events.

At this juncture when our domain is 'technically' being 'redefined' globally through multiple inflection points, Hyderabad—where it all began and which steered the direction at many a critical juncture, is there again to set the agenda of future for our technical progress through our conference. Coming out of teens couldn't be more fun, more exciting and more worth experiencing.

Registration Fees	Members: Price (INR/USD)		Non Members: Amount (INR/USD)	
	Up to Nov 30	From Dec 1	Up to Nov 30	From Dec 1
Conference	6500	7500	8000	9000
Tutorials 1 day	2500	3000	3000	3500
Tutorials 2 days	4500	5500	5500	6500



» [Click here](#) to start Registration

» Have a group of attendees to this event? Contact [secretary@vlsiconference.com](mailto:secretary@vlsiconference.com) for information on group registration.





4th - 8th JANUARY, 2008 at HICC, Hyderabad - India

[Call for Participation](#) | [Registration](#) | [Conference Program](#) | [Sponsorship Opportunities](#) | [Location](#) | [Team Members](#) | [Contact Us](#)

[Registration](#) ▾

[Home](#)

▾ [General Instructions](#)

▾ [Registration](#)

[Registration](#) ▾

[Personal Information](#)

\* Marked fields are mandatory

\* **Category** : Educational Institute

\* **Category Name** : abc (company / Institute Name)

\* **Title** : Mr.

\* **First Name** : john

\* **Last Name** : smith

\* **Address** : flat# 123

\* **City** : hyderabad

\* **Country** : India

\* **State/Province** : AP

\* **Pin Code/ Zip Code** : 500082

\* **Email Id** : john.smith@gmail.com

\* **Phone Number** : 12345678

Fax : 87654321

\* **Member of VLSI / IEEE / ACM** : No

\* **Number** :

\* **Select Tutorial** :  One Day  Two Days

\* **Conference** : (Jan. 6-8)  Yes  No

Tutorial 1 : Jan 4, 2006 T1

Tutorial 2 : Jan 5, 2006 Select

\* **Currency** :  INR

Price Payable : 11000.00

Total Price:  
(including service tax) : 12359.60

\* **Invoice in favour of** : Participating Member

\*\* click on Reset button if in case you selected wrong tutorials.





4th - 8th JANUARY, 2008 at HICC, Hyderabad - India

[Call for Participation](#) | [Registration](#) | [Conference Program](#) | [Sponsorship Opportunities](#) | [Location](#) | [Team Members](#) | [Contact Us](#)

[Registration](#) ▾

[Home](#)

▾ [General Instructions](#)

▾ [Registration](#)

[Registration](#) ▾

Registration

### Payment & Verification

Name of the Participating Member : john smith

Membership No. of VLSI / IEEE ACM :

Email id : john.smith@gmail.com

Tutorials Selected : Conference, Tutorial 1 -T1

Total Price : 12359.60 INR

Amount payable \* :  

\* marked fields are mandatory

**21<sup>st</sup> International  
Conference On VLSI Design**



**2008**



**7<sup>th</sup> International Conference  
On Embedded Systems**

**4th - 8th JANUARY, 2008 at HICC, Hyderabad - India**

### Make Payment

You have chosen to pay an amount of **Rs 12359.60** from your specified credit card for **VLSI trust 2007**.

<b>Temporary Registration Code</b>	<b>T1150</b>
<b>Full Name</b>	<b>john smith</b>
<b>Email ID</b>	<b>john.smith@gmail.com</b>
<b>Address</b>	<b>flat# 123</b>
<b>City</b>	<b>hyderabad</b>
<b>State</b>	<b>AP</b>
<b>Country</b>	<b>India</b>
<b>Phone Number</b>	<b>12345678</b>
<b>Total Amount</b>	<b>12359.60</b>

Please select your payment option and 'submit' form:

Select payment option

VISA



**Submit**

Powered by:





## Your payment request is being processed...

- This is a secure payment gateway using 128 bit SSL encryption
- When you submit the transaction, the server will take about 1 to 5 seconds to process, but it may take longer at certain times
- Please do not press "Submit" button once again or the "Back" or "Refresh" buttons.

**[www.billdesk.com](http://www.billdesk.com)**

©2004 IndiaIdeas.com Ltd.

**WELCOME TO  
CITIBANK PAYMENT GATEWAY**

Enjoy the security and privileges of using your Credit Card / Diners Card / Citibank ATM/Debit Card to make payments or for shopping online.

Kindly give us the following details to process your transaction

**Choose Your Card:**

-----Select----- 

This is a Secure payment gateway using 128-bit SSL encryption.  
Please read the applicable [terms and conditions](#).

# WELCOME TO CITIBANK PAYMENT GATEWAY

Kindly give us the following details to process your transaction for **Rs. 12359.60**

Type of Card:

Other Credit Card

Card Number:

(Please enter your non Citibank Master / Visa Card Card Number)

CVV Number:

(Kindly enter the last 3 digits of the number on the last back panel of your Credit Card. Only asterisks will appear as you enter them to ensure online security.)

Card Expiry Date :

Month  
01

Year  
05

[Submit](#)



This is a Secure payment gateway using 128-bit SSL encryption.  
Please read the applicable [terms and conditions](#).

## CITIBANK PAYMENT GATEWAY

You have made the following transactions on your card.

Thank you for choosing to make the following transaction using the secure Citibank Payment Gateway.

**Merchant Name :** INDIA IDEAS-VLSI TRUST

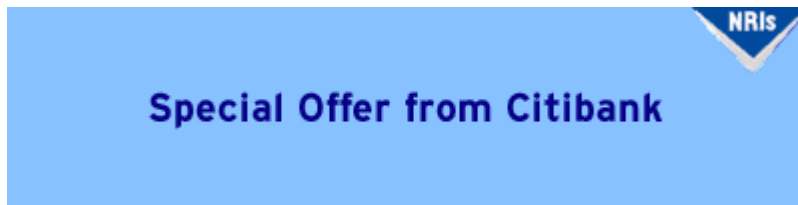
Order No.	Bank Ref No.	Amount	Auth. Decision
IT0711595965	595965	12359.60	Approved

Click to receive confirmation on your payments.

[Confirm](#)



**Approved -Amount Debited from the specified account**



This is a Secure payment gateway using 128-bit SSL encryption.  
Please read the applicable [terms and conditions](#).